

TANTALUM ELECTROLYTIC CAPACITORS

TMCU Series (Ultra Flat Low Profile Tantalum Chip Capacitors)

Low profile tantalum chip capacitors developed to meet the growing needs for flat capacitors where height is critical.

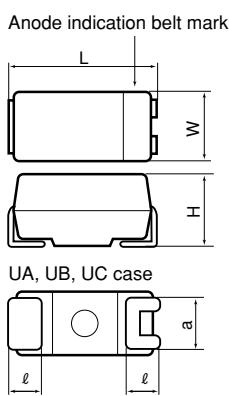
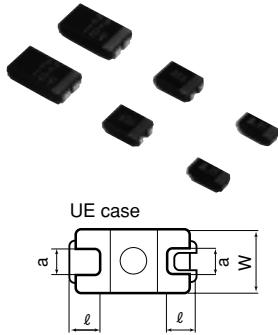
Features

- Small and low profile:
Obtained by thinning the TMCS type.

Product symbol : (Example) TMCU Series A case 4V 4.7 μ F ±20%

TMCU	A	0G	475	M	T	R
Type of series						
				Packing polarity code		
				Packing method code (T:carrier tape)		
				Capacitance tolerance code		
				Capacitance code		
				Rated voltage code		
				Case size code		

Outline of drawings and dimensions



Dimensions (Unit : mm)

Case code	Case size				
	L ± 0.2	W ± 0.2	H \max	l ± 0.3	a ± 0.2
UA	3.2	1.6	1.2	0.7	1.0
UB	3.5	2.8	1.2	0.7	1.8
UC	5.8	3.2	1.5	1.1	2.2
UE	7.3	4.3	1.9	1.3	2.4

Standard value and case size

Capacitance	Rated voltage (V.DC)						
	2.5	4	7	10	16	20	25
μ F	Code	0E	0G	0J	1A	1C	1D
0.10	104					UA	UA
0.15	154					UA	UA
0.22	224					UA	UA
0.33	334					UA	UA
0.47	474					UA	UA
0.68	684					UA	UA
1.0	105			UA	UA	UA,UB	
1.5	155		UA	UA	UA	UB	
2.2	225	UA	UA	UA	UA,UB	UB	
3.3	335	UA	UA	UA	UA,UB	UA,UB	UC
4.7	475	UA	UA	UA,UB	UA,UB	UB	UC
6.8	685	UA	UA,UB	UA,UB	UA,UB	UB,UC	UC
10	106	UA,UB	UA,UB	UA,UB	UA,UB,UC	UB,UC	
15	156	UA,UB	UA,UB	UA,UB,UC	UB,UC	UC	
22	226	UA,UB,UC	UA,UB,UC	UB,UC	UB,UC		
33	336	UB,UC	UB,UC	UB,UC	UB,UC		
47	476	UB,UC	UB,UC	UB,UC			
68	686	UB,UC	UB,UC				
100	107				UE		
150	157			UE			
220	227		UE				

Product specifications	TMCU			Test conditions JIS C5102-1994
	Operating temperature range	Rated voltage	Surge voltage	
Operating temperature range	-55°C ~ +125°C			
Rated voltage	DC2.5~35V		85°C	
Surge voltage	DC3.2~45V		85°C	
Derated voltage	DC1.6~22V		125°C	
Capacitance	0.1~220 μ F			
Capacitance tolerance	±10% or 20%			Paragraph 7.8, 120 Hz
Leakage current	0.01 CV or 0.5 μ A, whichever is larger or less.			Paragraph 7.7, in 5 minutes after the rated voltage is applied.
tan δ	1.0 or less 1.5~6.8 10~68 100~220	0.04 or less ≈2 0.06 or less 0.08 or less 0.12 or less		Paragraph 7.9, 120Hz
△C/C	±5% or less			
Surge withstanding voltage	Specified initial value or less			Paragraph 7.14
tan δ	Specified initial value or less			
LC	Specified initial value or less			
Temperature characteristics	Specified initial value or less	-55 85 125	0~+10% 0~+12%	Paragraph 7.12
△C/C	±2% or less			
tan δ	0.04 0.06 0.08 0.1 0.12	0.05 0.08 0.12 0.14 0.16	0.04 0.06 0.1 0.12 0.14	
LC	0.01CV or 5 μ A or less	-	0.1CV or 5 μ A or less	0.125CV or 6.25 μ A or less
Solder heat resistance	△C/C	±5% or less		Dip 260±5°C
	tan δ	Specified initial value or less		UA,UB case 10±1sec.
	LC	Specified initial value or less		UC case 5±0.5sec.
				Reflow—260°C 10±1sec.
Moisture resistance leaving	△C/C	±10% or less		Paragraph 9.5, 40°C
	tan δ	Specified initial value or less		90~95%RH,500h
	LC	Specified initial value or less		
High-temperature load	△C/C	±10% or less		Paragraph 9.10, 85°C
	tan δ	Specified initial value or less		The rated voltage is applied for 2000 hours.
	LC	125% Specified initial value or less		
Thermal shock	△C/C	±5% or less		Leave at -55°C, normal temperature, 125°C, and normal temperature for 30 min., 3 min., 30 min., and 3 min. Repeat this operation 5 times running.
	tan δ	Specified initial value or less		
	LC	Specified initial value or less		
Moisture resistance load	△C/C	±10% or less		40°C, humidity 90 to 95%RH
	tan δ	150% Specified initial value or less		The rated voltage is applied for 500 hours.
	LC	200% Specified initial value or less		
Failure rate	1% / 1000h			85°C. The rated voltage is applied (through a protective resistor of 1 Ω/V).

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Product name	tan δ
UB7V22 μ F, UB7V33 μ F, UB10V15 μ F, UB10V22 μ F	0.1
UB2.5V33 μ F, UB2.5V47 μ F, UB2.5V68 μ F, UB4V33 μ F, UB4V47 μ F	0.12

