

54LS08/DM54LS08/DM74LS08 Quad 2-Input AND Gates

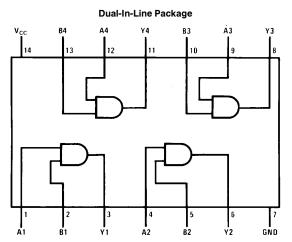
General Description

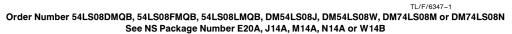
Features

This device contains four independent gates each of which performs the logic AND function.

 Alternate Military/Aerospace device (54LS08) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications.

Connection Diagram





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Function Table

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$\mathbf{Y} = \mathbf{A}\mathbf{B}$						
Inp	uts	Output				
Α	в	Y				
L	L	L				
L	н	L				
н	L	L				
н	Н	Н				
H = High Logic Level						



RRD-B30M105/Printed in U. S. A.

54LS08/DM54LS08/DM74LS08 Quad 2-Input AND Gates

June 1989

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	
DM54LS and 54LS	-55°C to +125°C
DM74LS	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54LS08			DM74LS08			Units
Gymbol	ratameter	Min	Nom	Max	Min	Nom	Max	onita
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High Level Input Voltage	2			2			v
VIL	Low Level Input Voltage			0.7			0.8	V
I _{OH}	High Level Output Current			-0.4			-0.4	mA
IOL	Low Level Output Current			4			8	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

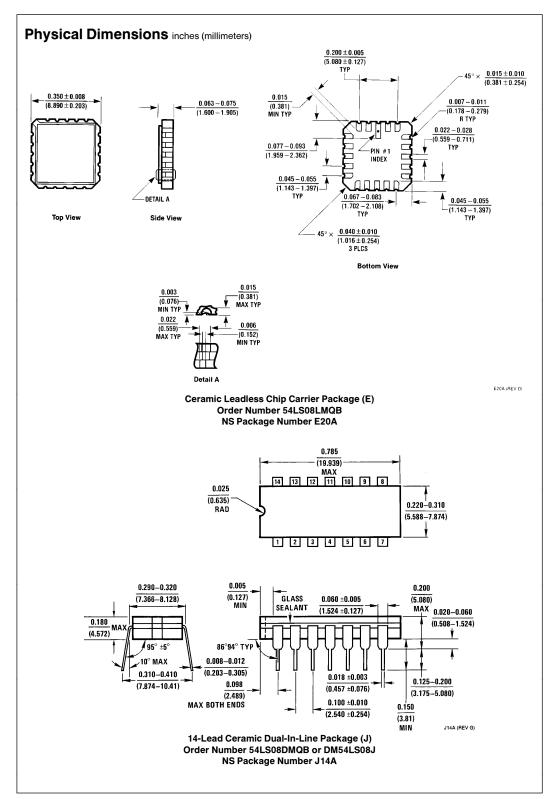
Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

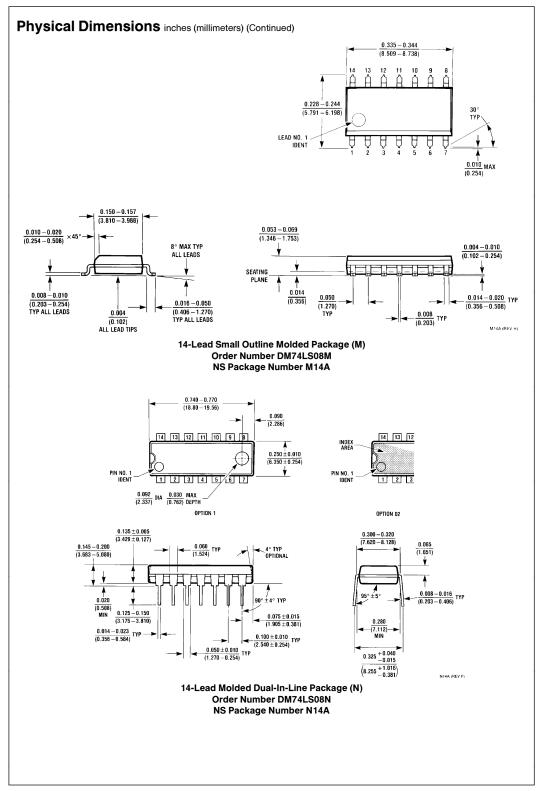
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Мах	Units
VI	Input Clamp Voltage	$V_{CC} = Min$, $I_I = -18 \text{ mA}$				-1.5	V
V _{OH}	High Level Output Voltage	$\label{eq:V_CC} \begin{split} V_{CC} &= \text{Min, } I_{OH} = \text{Max,} \\ V_{IH} &= \text{Min} \end{split}$	DM54	2.5	3.4		- V
			DM74	2.7	3.4		
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max,$	DM54		0.25	0.4	v
		V _{IL} = Max	DM74		0.35	0.5	
		$I_{OL} = 4 \text{ mA}, V_{CC} = Min$	DM74		0.25	0.4	
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$				0.1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μΑ
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-0.36	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	DM54	-20		-100	- mA
			DM74	-20		-100	
ICCH	Supply Current with Outputs High	V _{CC} = Max			2.4	4.8	mA
I _{CCL}	Supply Current with Outputs Low	V _{CC} = Max			4.4	8.8	mA

Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

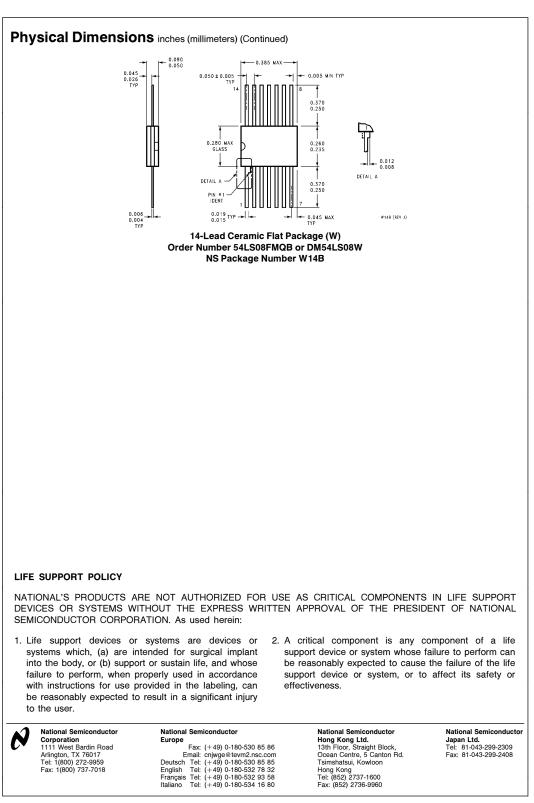
	Parameter						
Symbol		C _L = 15 pF		$C_L = 50 pF$		Units	
		Min	Max	Min	Мах		
t _{PLH}	Propagation Delay Time Low to High Level Output	4	13	6	18	ns	
t _{PHL}	Propagation Delay Time High to Low Level Output	3	11	5	18	ns	
Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.							

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.









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